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CERTIFICATION OF MAILING

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By:

Date:

January 14, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.: 10/076,977 Confirmation No: 1416
Applicant : Udo Hartmann
Filed : February 15, 2002
Title : Test System for Conducting a Function Test of a Semiconductor Element on a Wafer, and Operating Method
Art Unit : 2829
Examiner : Paresh H. Patel
Docket No. : P2001,0103 Customer No.: 24131

A M E N D M E N T

Mail Stop No Fee Amendment
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the Office Action dated October 15, 2003, kindly amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 4 of this paper.

Amendments to the Drawings begin on page 11 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 12 of this paper.

An **Appendix** including amended drawing figures is attached following page 22 of this paper.